



Applicant: Dempsey et al.
Docket No: AD-332J
Serial No: 10/627,500
Title: Integrated Digital Calibration Circuit and
Digital to Analog Converter (DAC)
Atty: Jason D. Shanske, Reg. No. 43,915

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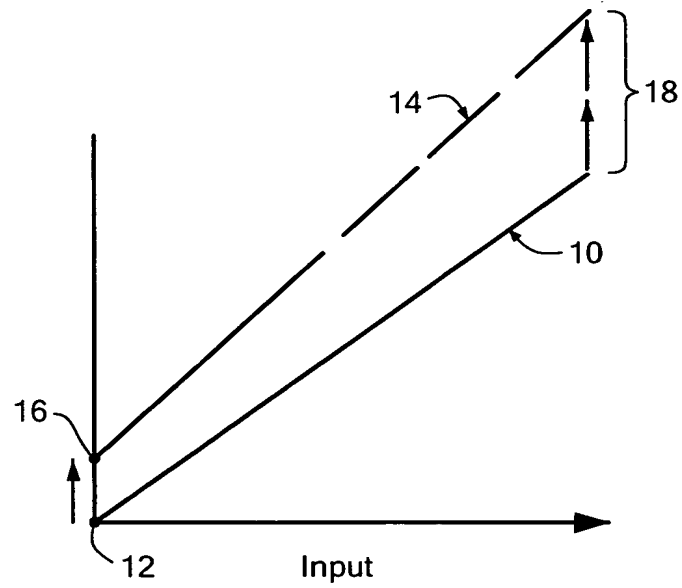


FIG. 1

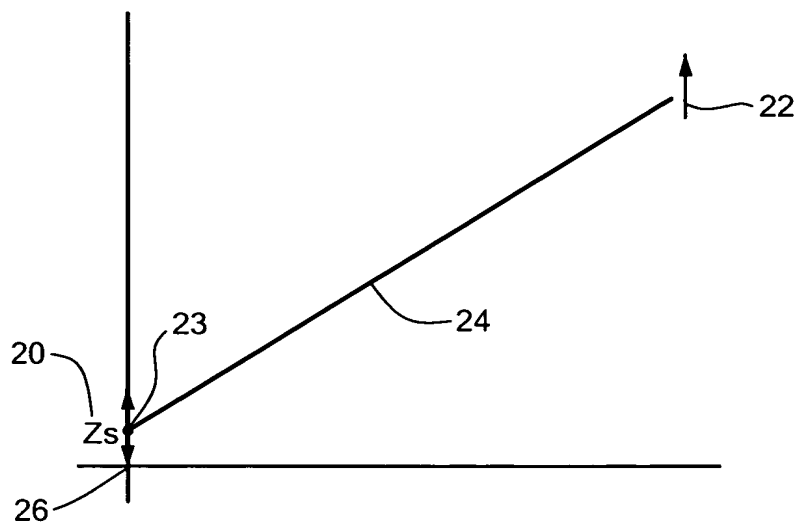
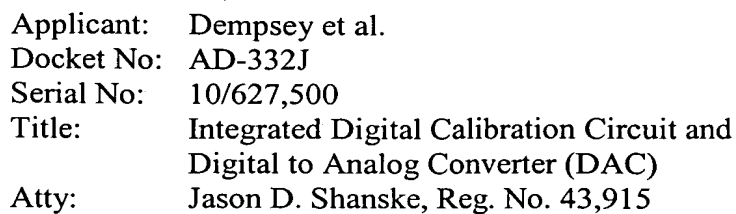


FIG. 2





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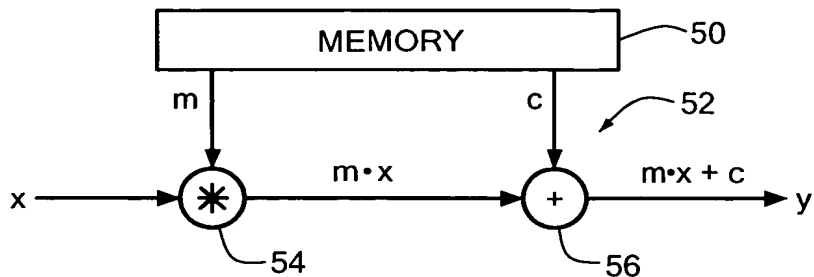


FIG. 4A

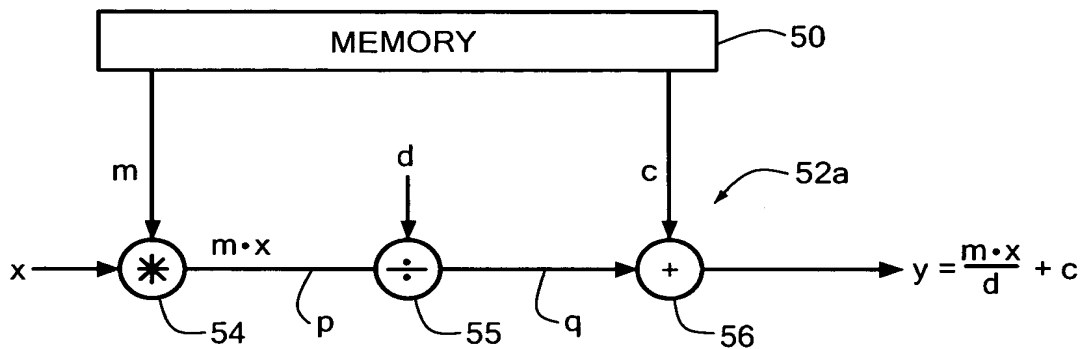


FIG. 4B

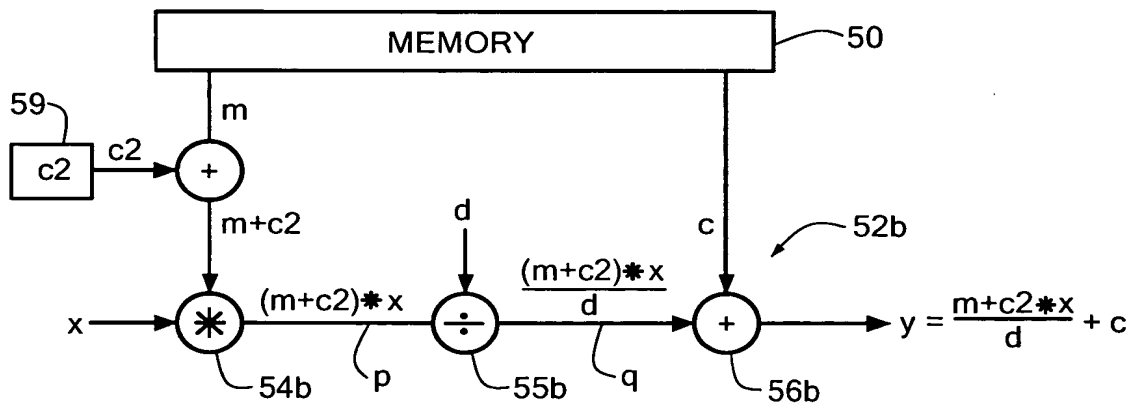
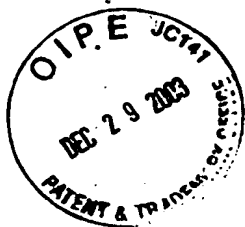


FIG. 4C



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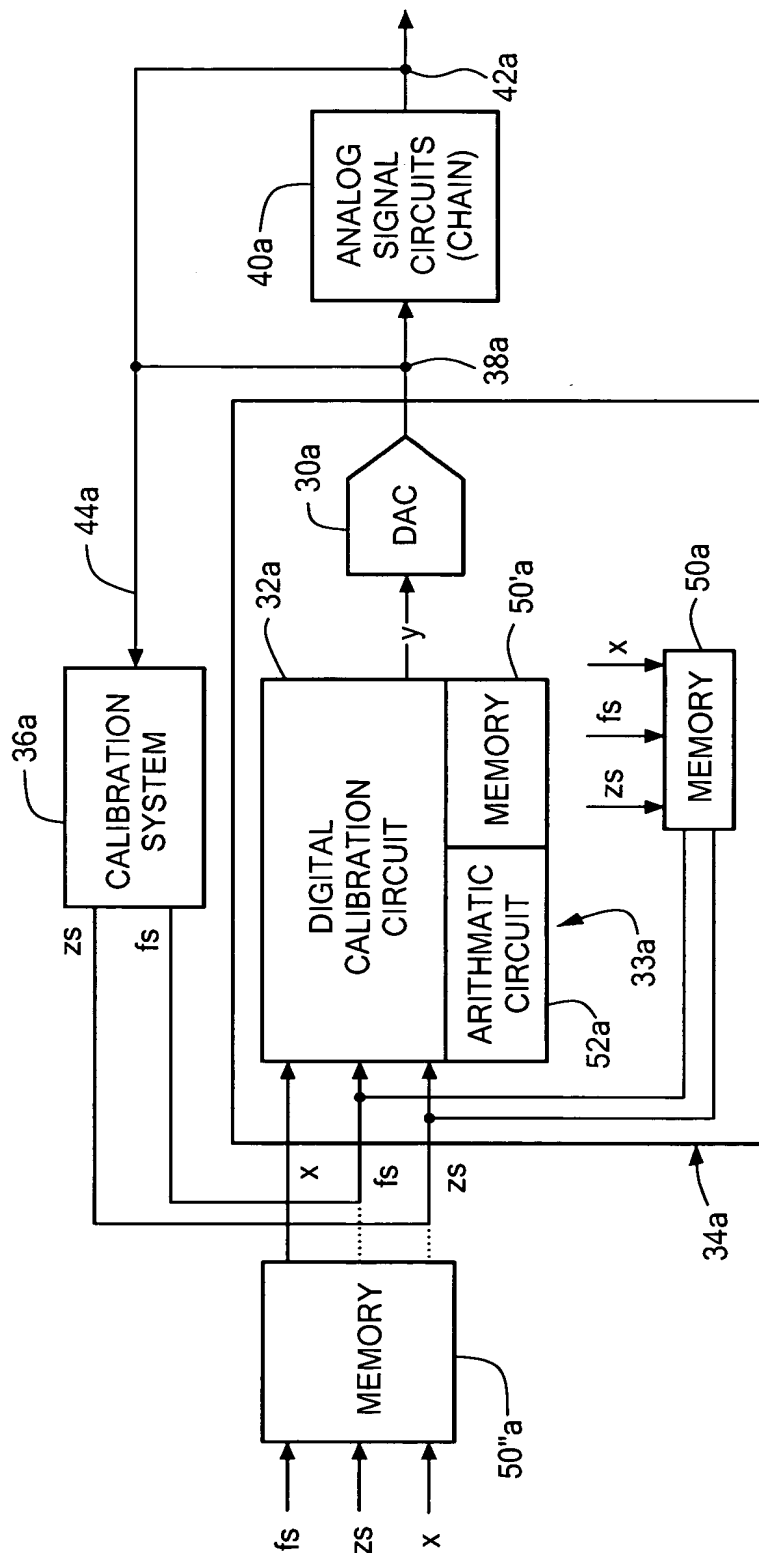


FIG. 5



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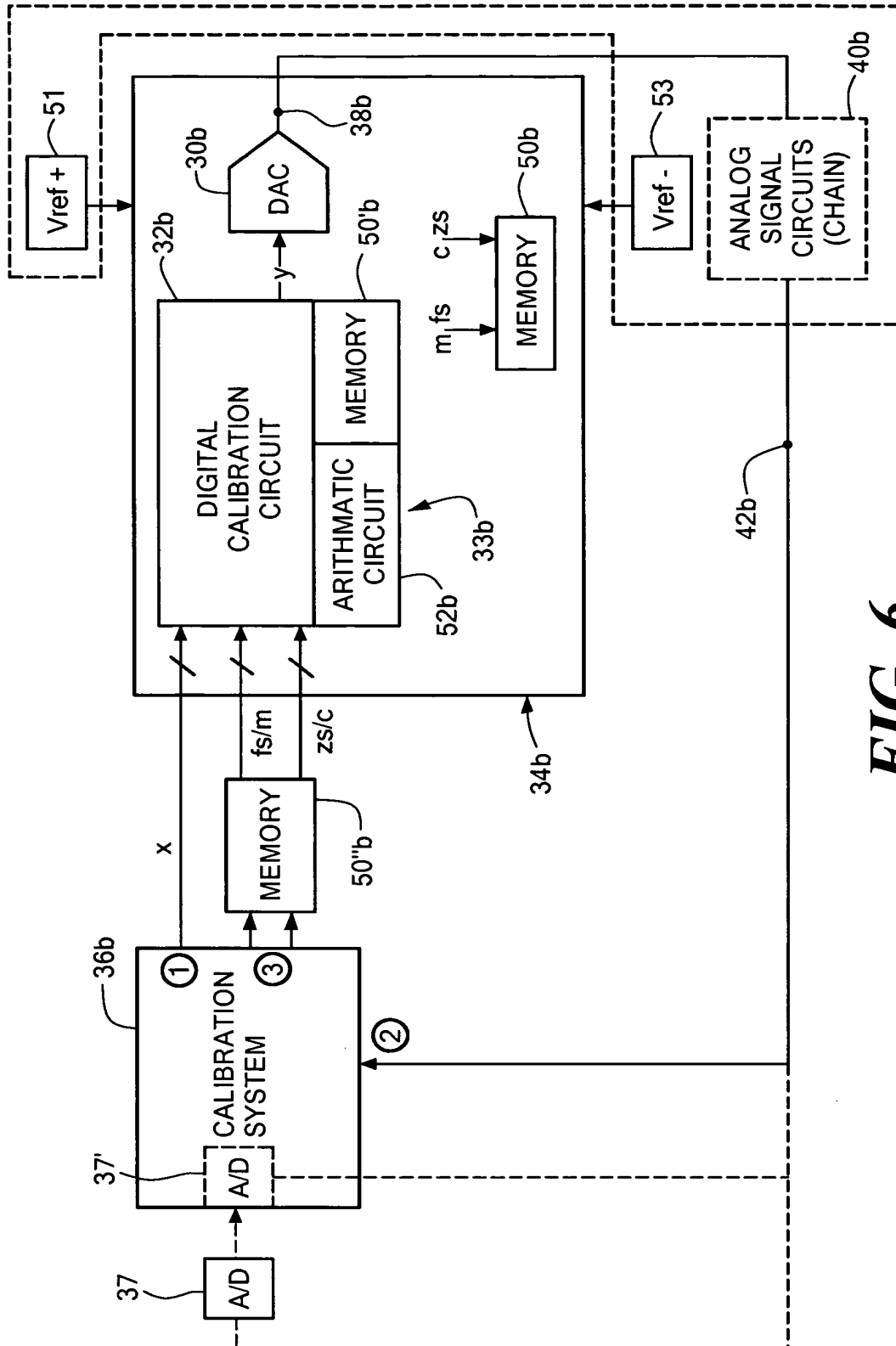


FIG. 6



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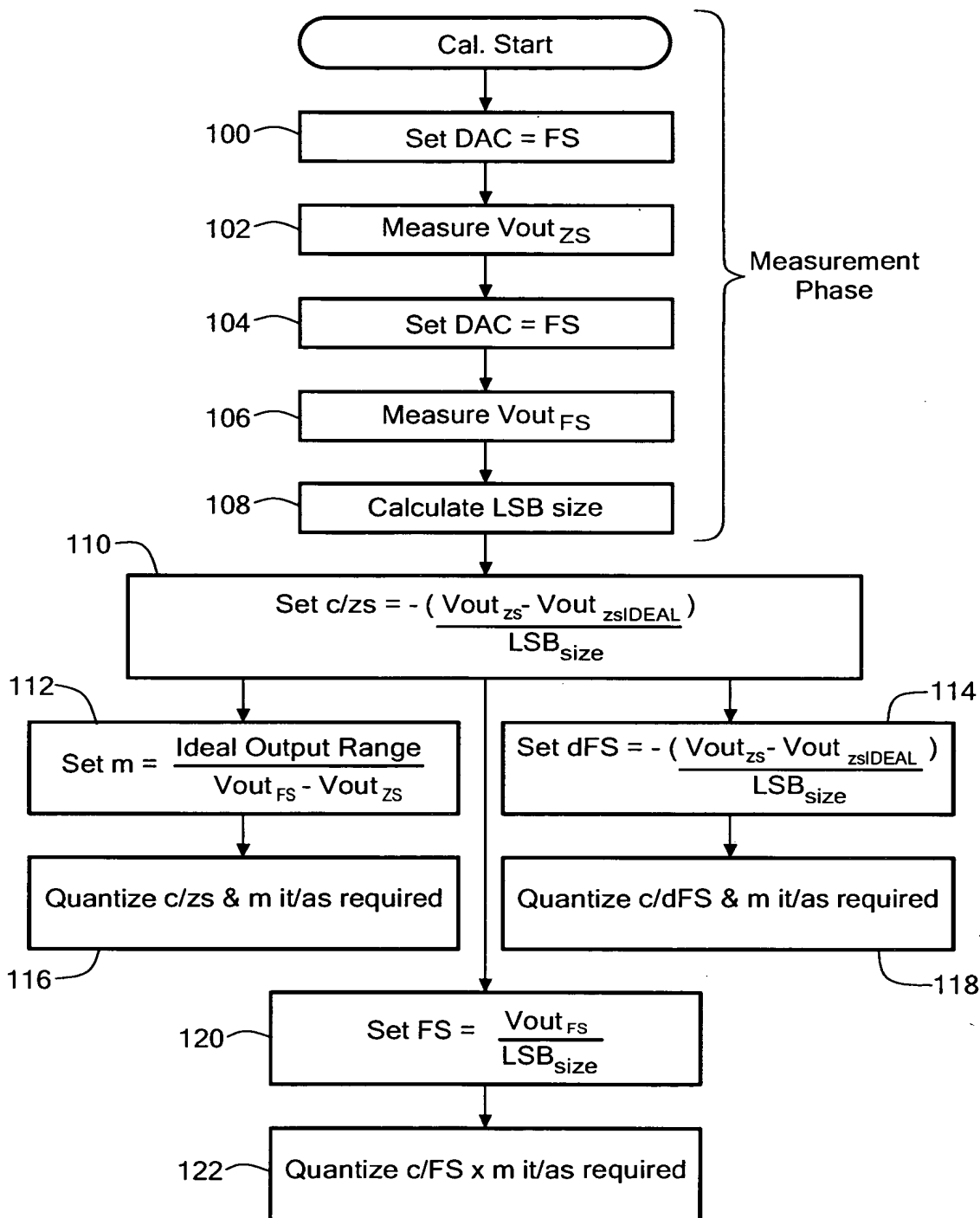


FIG 7